SUBJECT INDEX

Abstract algebra 27 (1989) 307
Abstract machines 27 (1989) 349
ADA 27 (1989) 221
Advanced workstations 27 (1989) 441
Algebraic transformation 27 (1989) 273
AND/OR trees 27 (1989) 391
Arithmetic codes 27 (1989) 697
Artificial intelligence 27 (1989) 69
Artificial intelligence system architectures 27 (1989) 25
ASICs 27 (1989) 101, 373, 473
Atomic actions 27 (1989) 521
Automated verification 27 (1989) 649
Automatic music recognition 27 (1989) 547
Automatic synthesis of systolic arrays 27 (1989) 479
Automatic test pattern generation 27 (1989) 675, 687

Bridging faults 27 (1989) 681

Callgraph analysis 27 (1989) 95
CAE tools 27 (1989) 583
CMOS fault behavior 27 (1989) 681
Coherent lightwave LANs 27 (1989) 489
Computer vision 27 (1989) 143, 147
Concurrency 27 (1989) 819
Concurrent error detection 27 (1989) 697
Concurrent systems 27 (1989) 417
Constraint logic programming 27 (1989) 649
Controllers 27 (1989) 53
Control flow 27 (1989) 627
Control part synthesis 27 (1989) 465

D-graphs 27 (1989) 527
Database models 27 (1989) 121
Database programming language 27 (1989) 19
Database semantics 27 (1989) 121
Data exchange formats 27 (1989) 687
Data flow analysis 27 (1989) 95
Data flow languages 27 (1989) 551
DBT transformation 27 (1989) 299
Dedicated architectures 27 (1989) 793
Demand driven control 27 (1989) 391
Denotational semantics 27 (1989) 607
Design maintenance 27 (1989) 63
Distributed applications 27 (1989) 327
Distributed languages 27 (1989) 199
Distributed operating systems 27 (1989) 811
Distributed programming 27 (1989) 455
Distributed real-time microprocessor systems 27 (1989) 665
Distributed systems 27 (1989) 221
Domain analysis 27 (1989) 639
DUNIX 27 (1989) 811
Dynamic load balancing 27 (1989) 423

Embedded systems 27 (1989) 239
Entity-relationship model 27 (1989) 113
Error minimization 27 (1989) 767
Ethernet IEEE 802.3 27 (1989) 757
Expert systems 27 (1989) 69, 757

Fault propagation 27 (1989) 799
Fault recovery 27 (1989) 801
Fault tolerance 27 (1989) 713
File-store architectures 27 (1989) 423
Formant synthesis 27 (1989) 557
Formal circuit description 27 (1989) 155
Formal verification 27 (1989) 171
Functional languages 27 (1989) 279
Functional programming 27 (1989) 593

GaAs architectures 27 (1989) 289
Graphical programming 27 (1989) 633
Graphical user interfaces 27 (1989) 455

Hardware automatic design 27 (1989) 367
Hardware correctness 27 (1989) 163
Hardware description language 27 (1989) 171, 267, 473
Hardware formal verification 27 (1989) 279
Hardware synthesis 27 (1989) 373
High level languages implementation 27 (1989) 307
High level synthesis 27 (1989) 465
Hypercube architectures 27 (1989) 501

Image processing 27 (1989) 147, 417
Image registration 27 (1989) 135
Incremental synthesis 27 (1989) 381
Industrial control 27 (1989) 705
Inter-process communications 27 (1989) 207, 215
Internetworking 27 (1989) 447

Knowledge based systems 27 (1989) 77

Local area networks 27 (1989) 447
Locking mechanisms 27 (1989) 431
Logical query languages 27 (1989) 127
Logic programming tools 27 (1989) 25

Machine automation 27 (1989) 723
Markovian models 27 (1989) 501
Massively parallel computers 27 (1989) 495
Man machine interface 27 (1989) 39
Memory hierarchies 27 (1989) 87
Microprocessor debugging 27 (1989) 619
Microprocessor simulation 27 (1989) 619
Microprogramming 27 (1989) 601
Motion 27 (1989) 53
Multiprocessing 27 (1989) 215, 251
Multiprocessor architectures 27 (1989) 147
Multiuser environments 27 (1989) 431
Multilevel Newton algorithm 27 (1989) 731
Multilevel simulation 27 (1989) 571
Music synthesis description 27 (1989) 551

Networks 27 (1989) 739
Neural networks 27 (1989) 189
Neural network description languages 27 (1989) 181
Neural network simulators 27 (1989) 181
Non invasive testing 27 (1989) 665

Object based systems 27 (1989) 713
Object modelling 27 (1989) 121
Object oriented databases 27 (1989) 31, 127
Object oriented modelling 27 (1989) 113
Object oriented programming languages 27 (1989) 31, 327
Object oriented systems 27 (1989) 19, 339
Object recognition 27 (1989) 39
Occam language 27 (1989) 405
Office networks 27 (1989) 441
Operating systems 27 (1989) 533
OPS5 language 27 (1989) 397

Parallelism 27 (1989) 207
Parallel processing 27 (1989) 397, 723
Parallel workstations 27 (1989) 731
Periodic maintenance 27 (1989) 657
Petri nets 27 (1989) 607
Phase coupling 27 (1989) 381
Photonic switching 27 (1989) 489
Picture generation 27 (1989) 627
PID controllers 27 (1989) 793
PLCs 27 (1989) 583
Preemption 27 (1989) 775
Process automation 27 (1989) 633
Programming environments 27 (1989) 31
Prolog language 27 (1989) 163

Rapid prototyping 27 (1989) 199
Real-time filters 27 (1989) 245
Real-time programs 27 (1989) 527
Real-time scheduling 27 (1989) 767, 775
Real-time UNIX 27 (1989) 533
Register transfer level 27 (1989) 367
Relational algebra 27 (1989) 19
Reliability analysis 27 (1989) 233
Resonator structures 27 (1989) 557
Reusability 27 (1989) 639
RISC architectures 27 (1989) 87
RISC processors 27 (1989) 101
Routing 27 (1989) 749
Rule-based systems 27 (1989) 749

Scheduling 27 (1989) 251
SCHEME Lisp dialect 27 (1989) 355
Sea-of-gates 27 (1989) 785
Semi-custom arrays 27 (1989) 565
Semi-custom VLSI 27 (1989) 69, 785
Service request models 27 (1989) 333
Signature monitoring 27 (1989) 705
Silicon compilers 27 (1989) 381
Single chip microcomputer applications 27 (1989) 579
Smalltalk language 27 (1989) 333
Software/hardware partitioning 27 (1989) 239
Software fault tolerance 27 (1989) 521
Spacecraft computers 27 (1989) 233
Speech synthesis 27 (1989) 363
Standard cells 27 (1989) 355
Static allocators 27 (1989) 315
Synchronization 27 (1989) 819
Synchronous data flow 27 (1989) 315
System level diagnosis 27 (1989) 799
System level simulation 27 (1989) 189
Systolic arrays 27 (1989) 299

Temporal logic 27 (1989) 541
Test development 27 (1989) 675
Time warp synchronization 27 (1989) 571
Transformational design 27 (1989) 261
Transient fault tolerance 27 (1989) 801
Transputers 27 (1989) 739
Transputer architecture 27 (1989) 339
Transputer implementations 27 (1989) 143
Transputer networks 27 (1989) 405

Vertical migration architectures 27 (1989) 289
Virtual machine implementations 27 (1989) 593
VLSI design 27 (1989) 77
VLSI design methodologies 27 (1989) 273
VLSI guided synthesis 27 (1989) 261
VLSI systems 27 (1989) 495

32-bit microprocessors 27 (1989) 349

AUTHOR INDEX

Agrawala, A.K., see Yuan, X., 27 (1989) 775
Alberts, L., Huijs, C., Maars, N., Spaanenburg, L., A Knowledge-Based Approach to VLSI Design in an Open CAD-Environment 27 (1989) 77
Alfs, G., Hartenstein, R.W., Wodtko, A., Explicit Fault Modeling and Hierachical Test Pattern Generation in the KARATE System 27 (1989) 675
Amblard, P., Charles, H.P., Music Synthesis Description with the Data Flow Language LUSTRE 27 (1989) 551
Ancona, M., Clematis, A., Gianuzzi, V., 32-bit Microprocessor Architectures and Extended Abstract Machines for High Level Languages 27 (1989) 349
Ancona, M., Giardina, L., Rollero, M.E., An Object-Manager for Handling Persistent Data in Modula-2 27 (1989) 31
Annunziata, M., Decarlini, G., Malaponti, C., NETPLAN: a Rule Based System to Design Packet Switching Data Networks 27 (1989) 749
Aposporidis, E., Lohnert, F., Mehring, P., Hoppe, F., Post, H.U., PMLS - a Parallel Multi-Level VLSI Simulator 27 (1989) 571
Auer, A., see Okkonen, A., 27 (1989) 513
Auer, A., see Reinikka, A., 27 (1989) 473
Avril, H., see Contreiras, F., 27 (1989) 39

Barro, S., see Zapata, E.L., 27 (1989) 299
Beer, M.D., The Use of an Office Information Server in an Environment based on High-Performance Workstations 27 (1989) 441
Beierle, C., see Bottcher, S., 27 (1989) 25
Ben-Tzvi, D., Sandler, M.B., Efficient Parallel Implementation of the Hough Transform on a Distributed Memory System 27 (1989) 147
Bergstraesser, T., see Gessner, J., 27 (1989) 465
Beunder, M., see Haas, W., 27 (1989) 565
Van der Bij, H., Smit, G., Havinga, P., Hardware Support for the TUMULT Real-Time Scheduler 27 (1989) 251
Billingsley, J., see Luk, B.L., 27 (1989) 47
Böttcher, S., Beierle, C., Database Support for the PROTOS-L System 27 (1989) 25
Boute, R., Syntactic and Semantic Aspects of Formal System Description 27 (1989) 155
Boute, R.T., see Claesen, L., 27 (1989) 261
Brezočnik, Z., Horvat, B., Formal Hardware Specification and Verification Using Prolog 27 (1989) 163
Brown, B.M., see Clarke, A.T., 27 (1989) 547
Bruggeman, F., see Luo, J., 27 (1989) 405
Bruguera, J.D., see Zapata, E.L., 27 (1989) 501
Buhler, P., Wybranietz, D., Tools for Distributed Programming in the INCAS Project 27 (1989) 199

Camurati, P., Margaria, T., Prinetto, P., Systolic Array Description in F^2 27 (1989) 171
Camurri, A., Temporal Logic Issues in Music Knowledge Representation 27 (1989) 541
Cantone, G., Esposito, A., A D-Graph Based Approach for Verifying Response Time of "Critical" Programs 27 (1989) 527
Carnal, D., see Nunez, A., 27 (1989) 289
Causarano, I., Guizzetti, R., Pipponzi, M., Somenzi, F., MSYN: Automatic Synthesis of Hardware 27 (1989) 367
Charles, H.P., see Amblard, P., 27 (1989) 551
Christodoulakis, D., see Goutas, S., 27 (1989) 127

Chung, J.Y., Shih, W.K., Liu, J.W.S., Gillies, D.W., Scheduling Imprecise Computations to Minimize Total Error 27 (1989) 767
Ciampolini, A., Corradi, A., Leonardi, L., Parallel Object System Support on Transputer-Based Architectures 27 (1989) 339
Claesen, L., see Ploegaerts, W., 27 (1989) 279
Claesen, L., Boute, R.T., De Man, J., Ploegaerts, W., Seutter, M., Vanslembrouck, J., Verkest, D., Application of System Semantics to VLSI for the Transformational Design of a Parameterized Booth Multiplier Module - a case study 27 (1989) 261
Clarke, A.T., Brown, B.M., Thorne, M.P., Coping with some really Rotten Problems in Automatic Music Recognition 27 (1989) 547
Clematis, A., see Ancona, M., 27 (1989) 349
Collado, M., Morales, R., Moreno, J., Conversation Modules: a Methodological Approach to the Design of Real-Time Applications 27 (1989) 521
Contreiras, F., Avril, H., Wilson, D., Winter, S., Synthesis of an Intelligent Environment for Object Recognition Using Tactile Data 27 (1989) 39
Corradi, A., see Ciampolini, A., 27 (1989) 339
Cortadella, J., see Jove', T., 27 (1989) 87
Crookes, D., Morrow, P., Sharif, B., Mc Clatchey, I., An Environment for Developing Concurrent Software for Transputer-based Image Processing 27 (1989) 417

Dasiewicz, P., see Parihar, R., 27 (1989) 63
De Man, H., see Ploegaerts, W., 27 (1989) 279
Decarlini, G., see Annunziata, M., 27 (1989) 749
Della Vigna, P., Faverio, P., Macchiavello, F., Mercalli, F., Scarabottolo, N., Tormene, G., Structure and Performances of a Campus-Wide IBM Token Ring Multi-Network 27 (1989) 447
Deussen, P., Rosenstiel, W., Schauser, K.E., Wedeck, J., Architecture Design of a RISC-Processor for Prolog 27 (1989) 101
Dirkx, E., IMISSED: Interactive Microprocessor Instruction Set Simulation Environment with Graphical Display 27 (1989) 619
Doallo, R., see Zapata, E.L., 27 (1989) 299
Dodrill, J., Kanopoulos, N., On the Design of a Real-Time Digital Median Filter 27 (1989) 245
Dowsing, R., Elliott, R., Templeton, M., Williams, G., Woodhams, F., A Framework for the Synthesis of Hardware from Occam 27 (1989) 373
De Bosschere, K., EDULAN: a Tool for Teaching Synchronization 27 (1989) 819
De Man, J., Vanslembrouck, J., Transformational Design of Digital Circuits 27 (1989) 273
De Man, J., see Claesen, L., 27 (1989) 261
Domenici, A., Lazzerini, B., Prete, C.A., A Protocol for Resource Locking and Deadlock Detection in a Multi-user Environment 27 (1989) 431
Du Yi-Ren, see Lin Wei, 27 (1989) 363

Eberbach, E., McCabe, S.C., Refenes, A.N., PARLE: a Language for Expressing Parallelism and Integrating Symbolic and Numeric Computations 27 (1989) 207
Elliott, R., see Dowsing, R., 27 (1989) 373
Esposito, A., see Cantone, G., 27 (1989) 527

Faverio, P., see Della Vigna, P., 27 (1989) 447
Fernandes, B., see Madeira, H., 27 (1989) 705
Fernin, P., see Flavell, S., 27 (1989) 135
Flavell, S., Winter, S., Wilson, D., Fernin, P., A Comparison of Frameworks for Cooperation in Distributed Image Registration Systems 27 (1989) 135
Frank, H., see Mühlhäuser, M., 27 (1989) 327

Frieder, O., Litman, A., Segal, M., DUNIX: Distributed Operating Systems Education via Experimentation 27 (1989) 811
Frisiani, A., see Maresca, M., 27 (1989) 495

Gessner, J., Bergstraesser, T., Wallstab, S., Synthesis of Control Units in a Design Environment for Chip Architecture 27 (1989) 465
Gianuzzi, V., see Ancona, M., 27 (1989) 349
Giardina, L., see Ancona, M., 27 (1989) 31
Gillies, D.W., see Chung, J.Y., 27 (1989) 767
Goncalves, G., see Hannequin, I., 27 (1989) 391
Gondzio, M., Verification Oriented Approach to Concurrent Microprogramming with the Language C-MIDDLE 27 (1989) 607
Van de Goor, A., see Levy, A.M., 27 (1989) 221
Van de Goor, A., see van Peursem, M., 27 (1989) 739
Goutas, S., Soupos, P., Christodoulakis, D., A New Approach Towards an Object-Oriented Database System 27 (1989) 127
Guizzetti, R., see Causarano, I., 27 (1989) 367

Haas, W., Beunder, M., Kernhof, J., Characteristics and Requirements of Semi-Custom Design Tools 27 (1989) 565
Halang, W.A., Languages and Tools for the Graphical and Textual System-Independent Programming of Programmable Logic Controllers 27 (1989) 583
Hannequin, I., Goncalves, G., Lecouffe, M., Toursel, B., Prolog: a New Parallel Evaluation Scheme 27 (1989) 391
Harrison, R., see Pu, J., 27 (1989) 53
Hartenstein, R.W., Lemmert, K., Riedmuller, M., Synthesis of Systolic Architectures Using the SYS^3 System 27 (1989) 479
Hartenstein, R.W., see Alfs, G., 27 (1989) 675
Havinga, P., see Smit, G., 27 (1989) 251
v.d. Heide, K., see Vogt, C. 27 (1989) 713
Heistermann, J., see Hulin, M., 27 (1989) 69
Heuser, L., see Mühlhäuser, M., 27 (1989) 327
Hiller, T., Process Programming Using Graphical Objects 27 (1989) 633
Hoefflinger, B., see Roos, G., 27 (1989) 785
Honkanen, E., see Jokitalo, P., 27 (1989) 143
Hoppe, F., see Aposporidis, E., 27 (1989) 571
Horvat, B., see Brezocnik, Z., 27 (1989) 163
Huang, K., On Location of Faults with Propagation to within a Set of Bounded Size 27 (1989) 799
Hughes, J.G., Microcomputer Implementations of an Object-Oriented Multiprocessing Database Programming Language 27 (1989) 19
Huijs, C., see Alberts, L., 27 (1989) 77
Hulin, M., , Heistermann, J., Partitioning of Digital Designs: a Knowledge Based Approach and Concepts for its Parallelization 27 (1989) 69

Ihme, T., A Reuse Base for Real-Time Software Specifications 27 (1989) 639

Jokitalo, P., Honkanen, E., Kaisto, I., Moring, I., Palo, H., Transputer Based Digital Signal Processing Unit for a 3-D Vision System 27 (1989) 143
Jové , T., Cortadella, J., Reduced Instruction Buffer for RISC Architectures 27 (1989) 87
Just, J., Romaniuk, R., Highly Parallel Distributed Computing Systems with Optical Interconnections 27 (1989) 489

Kaarela, K., see Rautiola, K., 27 (1989) 723
Kaisto, I., see Jokitalo, P., 27 (1989) 143
Kalaoja, J., see Okkonen, A., 27 (1989) 513
Kanopoulos, N., see Dodrill, J., 27 (1989) 245
Van Katwijk, J., see Levy, A.M., 27 (1989) 221
Kelly, M.A., Seviora, R.E., Performance of OPS5 Matching on CUPID 27 (1989) 397
Kernhof, J., see Haas, W., 27 (1989) 565
Kienhöfer, J., see Mühlhäuser, M., 27 (1989) 327
Knoppers, P., see van Peursem, M., 27 (1989) 739

Korb, T., Zell, A., A Declarative Neural Network Description Language 27 (1989) 181

Lazzerini, B., see Domenici, A., 27 (1989) 431
Lecouffe, M., see Hannequin, I., 27 (1989) 391
Leenstra, J., see Roos, G., 27 (1989) 785
Lemmert, K., see Hartenstein, R.W., 27 (1989) 479
Leonardi, L., see Ciampolini, A., 27 (1989) 339
Levanto, M., see Okkonen, A., 27 (1989) 513
Levy, A.M., van de Goor, A.J., van Katwijk, J., Distributed System Design Using Ada as a Tool for Prototyping 27 (1989) 221
Li Zhi-Zhu, see Lin Wei, 27 (1989) 363
Litman, A., see Frieder, O., 27 (1989) 811
Liu, J.W.S., see Chung, J.Y., 27 (1989) 767
Lin Wei, Du Yi-Ren, Li Zhi-Zhu, A Design of Microcomputer Speech Output Device 27 (1989) 363
Lohnert, F., see Aposporidis, E., 27 (1989) 571
Lohse, J., Rauscher, R., Schütz, B., A Coprocessor for Implementing the LISP-Dialect SCHEME 27 (1989) 355
Luk, B.L., Naghdy, F., Billingsley, J., Stochastic Force Sensing Application in Robotics 27 (1989) 47
Luo, J., Bruggeman, F., Reijns, G.L., A Flexible Transputer Network for Numerical Applications 27 (1989) 405

Maars, N., see Alberts, L., 27 (1989) 77
Macchiavello, F., see Della Vigna, P., 27 (1989) 447
Madeira, H., Fernandes, B., Rela, M., Silva, J., The Fault-Tolerant Architecture of the SAFE System 27 (1989) 705
Malaponti, C., see Annunziata, M., 27 (1989) 749
Maresca, M., Frisiani, A., Vernazza, T., Floating Point Acceleration in Massively Parallel Architectures 27 (1989) 495
Margaria, T., see Camurati, P., 27 (1989) 171
Marino, P., Maxion, R., Expert System Design for Symptom-Based Diagnosis in Local Area Networks 27 (1989) 757
Marinos, L., see Papazoglou, M., 27 (1989) 113
Marwedel, P., Schenk, W., Improving the Performance of High-Level Synthesis 27 (1989) 381
Mattern, F., Sturm, P., An Automatic Distributed Calendar and Appointment System 27 (1989) 455
Maxion, R., see Marino, P., 27 (1989) 757
Mc Clatchey, I., see Crookes, D., 27 (1989) 417
McCabe, S.C., see Eberbach, E., 27 (1989) 207
Mehring, P., see Aposporidis, E., 27 (1989) 571
Mercalli, F., see Della Vigna, P., 27 (1989) 447
Mizuhashi, Y., Teramoto, M., Real-Time UNIX Operating System: RX-UX 832 27 (1989) 533
Mok, A., Rosier, L., Tulchinsky, I., Varvel, D., Algorithms and Complexity of the Periodic Maintenance Problem 27 (1989) 657
Moore, P.R., see Pu, J., 27 (1989) 53
Morales, R., see Collado, M., 27 (1989) 521
Moreno, J., see Collado, M., 27 (1989) 521
Moring, I., see Jokitalo, P., 27 (1989) 143
Morrow, P., see Crookes, D., 27 (1989) 417
Mühlhäuser, M., Schill, A., Kienhofer, J., Frank, H., Heuser,L., A Software Engineering Environment for Distributed Applications 27 (1989) 327
Mulder, J.M., Inter: an Inexpensive Inter-Procedural Register Allocator 27 (1989) 95
Muylaert Filho, J., Tavares Fernandes, E., Microprogrammed Implementation of a Reduction Machine 27 (1989) 593

Naghdy, F., see Luk, B.L., 27 (1989) 47
Nayha, T., see Rautiola, K., 27 (1989) 723
Nijhuis, J., Spaanenburg, L., Warkowski, F., Structure and Application of NNSIM: a General-Purpose Neural Network SIMulator 27 (1989) 189
Nolin, L., see Pissaloux, E., 27 (1989) 307

Novak, F., see Savnik, I., 27 (1989) 121
Nuñez, A., Carnal, D., MVM: a GaAs Microprocessor for Critical Real-Time Applications 27 (1989) 289
Nylén, P., An Integrated Development System for the MC68705 Single Chip Computer 27 (1989) 579

O'Dowd, M., MEX, a Programming Language that Supports Discrete Event Simulation 27 (1989) 215
Okkonen, A., Auer, A., Levanto, M., Okkonen, J., Kalaoja, J., SOKRATES-SA: a Formal Method for Specifying Real-Time Systems 27 (1989) 513
Okkonen, A., see Reinikka, A., 27 (1989) 473
Okkonen, J., see Okkonen, A., 27 (1989) 513
Oolman, R., Seutter, M., van Reeuwijk, C., Glass, a Language for Analog and Digital Circuit Description, and its Environment 27 (1989) 267
Ostroff, J.S., Mechanizing the Verification of Real-Time Discrete Systems 27 (1989) 649

Palo, H., see Jokitalo, P., 27 (1989) 143
Papazoglou, M., Marinos, L., On Integrating Database Modeling Constructs for Software Engineering Databases 27 (1989) 113
Parihar, R., Dasiewicz, P., A Methodology for Structural and Behavioural Understanding of Digital Systems 27 (1989) 63
Petkov, A., Tzonev, T., DIPROTALK: a Tool for Object-Oriented Design of Distributed Applications 27 (1989) 333
van Peursem, M., Knoppers, P., van de Goor, A., TLS: a System for Building and Controlling Transputer Networks 27 (1989) 739
Pfeifer, J., Wahl, M., Wojtkowiak, H., Data Exchange Formats for Testing 27 (1989) 687
Pipponzi,M., see Causarano, I., 27 (1989) 367
Pissaloux, E., Nolin, L., NL1 Machine - a Concept of a High Performance Data Type Architecture 27 (1989) 307
Piuri, V., Optimum Design of Fault-Tolerant Arithmetic Array Processors by Using Data Coding 27 (1989) 697
Plata, O.G., see Zapata, E.L., 27 (1989) 501
Ploegaerts, W., Verkest, D., Claesen, L., De Man, H., Description and Verification of More-Dimensional Regular and non-Homogeneous Structures using a Functional Hardware Description Language 27 (1989) 279
Ploegaerts, W., see Claesen, L., 27 (1989) 261
Post, H.U., see Aposporidis, E., 27 (1989) 571
Preece, C., see Wingate, G., 27 (1989) 801
Prete, C.A., see Domenici, A., 27 (1989) 431
Prinetto, P., see Camurati, P., 27 (1989) 171
Pu, J., Harrison, R., Weston, R.H., Moore, P.R., Software Design of Distributed Single-axis Motion Controllers for Industrial Applications 27 (1989) 53

Rauscher, R., see Lohse, J., 27 (1989) 355
Rautiola, K., Nayha, T., Kaarela, K., Parallel Processing in Machine Automation 27 (1989) 723
van Reeuwijk, C., see Oolman, R., 27 (1989) 267
Refenes, A.N., see Eberbach, E., 27 (1989) 207
Reijns, G.L., see Luo, J., 27 (1989) 405
Reinikka, A., Auer, A., Okkonen, A., Automated Synthesis of Structural HDL Descriptions from Graphic Specification of Embedded ASICs 27 (1989) 473
Rela, M., see Madeira, H., 27 (1989) 705
Reus, T., Acceleration of Circuit Simulation on a Parallel Transputer Workstation 27 (1989) 731
Riedmuller, M., see Hartenstein, R.W., 27 (1989) 479
Rivera, F.F., see Zapata, E.L., 27 (1989) 501
Robič, B., see Šilc, J., 27 (1989) 315
Rollero, M.E., see Ancona, M., 27 (1989) 31
Romaniuk, R., see Just, J., 27 (1989) 489

Roos, G., Leenstra, J., Schwederski, T., Spaanenburg, L., Hoefflinger, B., On Structured Gate Forest VLSI Design 27 (1989) 785
Rosenstiel, W., see Deussen, P., 27 (1989) 101
Rosier, L., see Mok, A., 27 (1989) 657

Sandler, M.B., High Complexity Resonator Structures for Formant Synthesis of Musical Instruments 27 (1989) 557
Sandler, M.B., see Ben-Tzvi, D., 27 (1989) 147
Santana, M.J., Zaluska, E.J., A High-Level Coordinator in a Distributed File-Store Architecture 27 (1989) 423
Sarwar, G., see Suchenek, M.A., 27 (1989) 665
Savnik, I., Novak, F., A Construction Database Model 27 (1989) 121
Scarabottolo, N., see Della Vigna, P., 27 (1989) 447
Schauser, K.E., see Deussen, P., 27 (1989) 101
Schenk, W., A High Speed Prolog Implementation on a VLIW Processor 27 (1989) 601
Schenk, W., see Marwedel, P., 27 (1989) 381
Schill, A., see Mühlhäuser, M., 27 (1989) 327
Schult, U., A Graphics-Only Programming Tool 27 (1989) 627
Schütz, B., see Lohse, J., 27 (1989) 355
Schwederski, T., see Roos, G., 27 (1989) 785
Segal, M., see Frieder, O., 27 (1989) 811
Seutter, M., see Claesen, L., 27 (1989) 261
Seutter, M., see Oolman, R., 27 (1989) 267
Seviora, R.E., see Kelly, M.A., 27 (1989) 397
Sharif, B., see Crookes, D., 27 (1989) 417
Shih, W.K., see Chung, J.Y., 27 (1989) 767
Šilc, J., Robic, B., Synchronous Dataflow-Based Architecture 27 (1989) 315
Silva, J., see Madeira, H., 27 (1989) 705
Sipola, M., see Soininen, J.P., 27 (1989) 239
Smit, G., see Van der Bij, H., 27 (1989) 251
Soininen, J.P., Sipola, M., Tiensyrja, K., SW/HW-Partitioning of Real-Time Embedded Systems 27 (1989) 239
Somenzi, F., see Causarano, I., 27 (1989) 367
Soupos, P., see Goutas, S., 27 (1989) 127
Spaanenburg, L., see Nijhuis, J., 27 (1989) 189
Spaanenburg, L., see Roos, G., 27 (1989) 785
Spaanenburg, L., see Alberts, L., 27 (1989) 77
Storti Gajani, G., Some Proposals for VLSI Implementation of Digital PID Controllers with some Fault-Tolerance Capabilities 27 (1989) 793
Strömmer, E., A Fault Tolerant Control Computer for the ASPERA Plasma Instrument of Phobos Spacecraft 27 (1989) 233
Sturm, P., see Mattern, F., 27 (1989) 455
Suchenek, M.A., Zheng, X., Sarwar, G., Non-Invasive Testing of Real-Time Avionic μ-Processor Systems 27 (1989) 665

Tavares Fernandes, E., see Muylaert Filho, J., 27 (1989) 593
Templeton, M., see Dowsing, R., 27 (1989) 373
Teramoto, M., see Mizuhashi, Y., 27 (1989) 533
Thorne, M.P., see Clarke, A.T., 27 (1989) 547
Tiensyrjä, K., see Soininen, J.P., 27 (1989) 239
Tormene, G., see Della Vigna, P., 27 (1989) 447
Toursel, B., see Hannequin, I., 27 (1989) 391
Tulchinsky, I., see Mok, A., 27 (1989) 657
Tzonev, T., see Petkov, A., 27 (1989) 333

Vanslembrouck, J., see De Man, J., 27 (1989) 273
Vanslembrouck, J., see Claesen, L., 27 (1989) 261
Varvel, D., see Mok, A., 27 (1989) 657
Verkest, D., see Claesen, L., 27 (1989) 261
Verkest, D., see Ploegaerts, W., 27 (1989) 279
Vernazza, T., see Maresca, M., 27 (1989) 495
Vierhaus, H., Testability of Non-Trivial CMOS Faults under Realistic Conditions 27 (1989) 681
Vogt, C., PUMA: Hardware Support for Fault-Tolerance and Object-Oriented Programming 27 (1989) 713

Wahl, M., see Pfeifer, J., 27 (1989) 687
Wallstab, S., see Gessner, J., 27 (1989) 465
Warkowski, F., see Nijhuis, J., 27 (1989) 189
Wedeck, J., see Deussen, P., 27 (1989) 101
Weston, R.H., see Pu, J., 27 (1989) 53
Williams, G., see Dowsing, R., 27 (1989) 373
Wilson, D., see Contreiras, F., 27 (1989) 39
Wilson, D., see Flavell, S., 27 (1989) 135
Wingate, G., Preece, C., Performance Evaluation of a New Design Tool for Microprocessor Transient Fault Recovery 27 (1989) 801
Winter, S., see Contreiras, F., 27 (1989) 39
Winter, S., see Flavell, S., 27 (1989) 135
Wodtko, A., see Alfs, G., 27 (1989) 675
Wojtkowiak, H., see Pfeifer, J., 27 (1989) 687
Woodham, F., see Dowsing, R., 27 (1989) 373
Wybranietz, D., see Buhler, P., 27 (1989) 199

Yuan, X., Agrawala, A.K., Real-Time Scheduling with both Preemption and Nonpreemption Requirements 27 (1989) 775

Zaluska, E.J., see Santana, M.J., 27 (1989) 423
Zapata, E.L., Bruguera, J.D., Plata, O.G., Rivera, F.F., A Parallel Markovian Model Reliability Algorithm for Hypercube Networks 27 (1989) 501
Zapata, E.L., Doallo, R., Barro, S., A DBT-Based VLSI Systolic Architecture for Hard Squared Error Clustering 27 (1989) 299
Zell, A., see Korb, T., 27 (1989) 181
Zheng, X., see Suchenek, M.A., 27 (1989) 665